



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2325

SERIAL NUMBER 09/944,134	FILING DATE 09/04/2001 RULE	CLASS 375	GROUP ART UNIT 2631	ATTORNEY DOCKET NO. 500.40612X00
-----------------------------	-----------------------------------	--------------	------------------------	-------------------------------------

## APPLICANTS

Mitsugu Kusunoki, Kunitachi, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

NONE JAT

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-266971 09/04/2000

YES JAT

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/03/2001

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i> Initials <i>JAT</i>	JAPAN	13	11	1

## ADDRESS

24956  
 MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.  
 1800 DIAGONAL ROAD  
 SUITE 370  
 ALEXANDRIA, VA  
 22314

✓

## TITLE

Semiconductor integrated circuit with suppressed clock skew

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
RECEIVED 710		